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RESYNCHRONIZATION | Embedded Multiprocessors | Taylor ...

Embedded Multiprocessors Scheduling and Synchronization Sundararajan Sriram , Texas Instruments, Inc., Dallas, Texas, and Shuvra S. Bhattacharyya , University of Maryland, College Park series: Signal Processing and Communications volume: 3 03/01/2000 In Press Hardcover, 352 Pages, Illustrated ISBN: 0-8247-9318-8 World Price: $ 150.00 ENGINEERING

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Embedded Multiprocessors: Scheduling and Synchronization (Signal Processing and Communications) Sathya Sriram, Shuvra S. Bhattacharyya. Embedded MultiprocessorsScheduling and SynchronizationSeries Volume: 3 This item is part of the Signal Processing and Communications series. Application-specific, embedded multiprocessors are increasingly found today in high-performance communications and multimedia systems such as cellular phones and base stations, high-speed modems, set-top boxes, and ...

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Abstract: Inter-processor communication and synchronization are critical problems in embedded multiprocessors. In order to achieve high-speed communication and low-latency synchronization, most recent designs employ dedi-cated hardware engines to support these communication protocols individually, which is complex, inflexible, and error prone.

Optimized Communication and Synchronization for Embedded ...

Marilyn Wolf, in High-Performance Embedded Computing (Second Edition), 2014. 5.10 Summary. Multiprocessors are at least as important in embedded computing as they are in scientific computing. Multiprocessors are critical to the delivery of real-time computation; they can also be more energy efficient than uniprocessors.

Multiprocessors - an overview | ScienceDirect Topics

Bhattacharyya has also co-authored and co-edited several books, including "The Handbook of Signal Processing Systems" (Springer; second edition: 2013, first edition: 2010), "Embedded Multiprocessors: Scheduling and Synchronization" (second edition: CRC Press, 2009, first edition: Marcel Dekker, Inc., 2000), "Embedded Computer Vision" (Springer ...

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